

**REMARKS**

The Office Action dated March 27, 2006 has been carefully considered. Claims 4-6, 21, 76 and 77 have been amended. Claims 2, 4-19, 21-25, 28-57, 66-77 are in this application.

Claims 8-19, 28-57 and 66-75 are allowed.


Claims 4-6, 21, 76 and 77 were rejected under 35 U.S.C. § 112 as indefinite. Applicants have amended the claims to obviate the Examiner's rejection. Support for this amendment is found through out the specification and in particular on page 8, line 21 to page 9, line 3. No new matter has been added.

Applicants note that the present invention divides the destination register into groups of bits, for example, 8 bits. Thereafter, any 8 bits from the source register are selected and permuted into the group of contiguous bits, i.e., 8 bits, in the destination register. Then, the steps are repeated for the next group of bits, i.e., 8 bits, in the destination register.

In view of the foregoing, Applicants submit that all pending claims are in condition for allowance and request that all claims be allowed. The Examiner is invited to contact the undersigned should he believe that this would expedite prosecution of this application. It is believed that no fee is required. The Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 13-2165.

Respectfully submitted,

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